

WHAT THE INVENTION CLAIMED IS

1. A non-volatile memory storage integrated circuit comprising a controller IC, a NAND IC and a memory IC, wherein said controller IC has a USB transmission interface terminal.
- 5 2. The non-volatile memory storage integrated circuit according to claim 1, wherein said controller IC comprises an I/O controller interface terminal.
3. The non-volatile memory storage integrated circuit according to claim 1, wherein said controller IC comprises a memory card interface terminal.
4. The non-volatile memory storage integrated circuit according to claim 2, 10 wherein said controller IC comprises a memory interface terminal and an I/O controller interface terminal.
5. The non-volatile memory storage integrated circuit according to claim 3, wherein said controller IC comprises a USB transmission interface terminal and a memory interface terminal.
- 15 6. The non-volatile memory storage integrated circuit according to claim 1, wherein said controller IC comprises an I/O controller interface terminal and a memory card interface terminal.
7. The non-volatile memory storage integrated circuit according to claim 6, said controller IC comprises a memory card interface terminal, a USB transmission interface 20 terminal and a memory interface terminal.
8. The non-volatile memory storage integrated circuit according to claim 1, wherein said controller IC comprises a built-in ECC and calculation means.

9. The non-volatile memory storage integrated circuit according to claim 1,
wherein said memory IC is a NOR IC or a SRAM IC.

10. The non-volatile memory storage integrated circuit according to claim 1,
wherein said non-volatile memory storage integrated circuit is packaged into a Chip Size
5 Package (CSP).

11. The non-volatilized memory storage integrated circuit according to claim 1,
wherein said non-volatile memory storage integrated circuit is packaged into a Ball Grid
Array (BGA).